

IRW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2823
Examiner: William D. Coleman

In re PATENT APPLICATION of:

Applicant : Toshinori GATO

Appln. No. : 10/619,003

Filed : July 15, 2003

For : METHOD OF MANUFACTURING
A SEMICONDUCTOR DEVICE HAVING
STACKED SEMICONDUCTOR CHIPS
(As Amended)

Attorney Ref. : OKI 278 C1

AMENDMENT

May 11, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Amendment is submitted in response to the non-final Office Action dated February 15, 2004, the time for response to which is up to and including May 16, 2005.

Please amend the application in accordance with the following:

Amendments to the Specification beginning on page 2;

Amendments to the Drawings beginning on page 3; and

Amendments to the Claims beginning on page 4.

FEE ENCLOSED: \$
Please charge any further
fee to our Deposit Account
No. 18-0002